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MULTIPLE CHOICE. Choose the one alternative that best completes the statement or answers the question.

1) For this circuit, determine the load-line intersection with the two axis.

1) _____

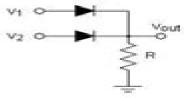
2)



A) VD = 10 V and ID = 1 mA C) VD = 10 V and ID = 10 mA

- B) VD = 1 V and ID = 1 mA
- D) $^{\mathrm{VD}}$ = 1 V and $^{\mathrm{ID}}$ = 10 mA.
- If one silicon diode and one germanium diode are connected in series, the voltage drop across the combination of the two diodes will be equal to ______
 - A) the forward drop equal to that of the silicon diode
 - B) the forward drop equal to that of the difference of the voltage drops across the two diodes
 - C) the forward drop equal to that of the sum of the voltage drops across the two diodes
 - D) the forward drop equal to that of the germanium diode
- 3) Name the logic gate that is formed by this circuit.

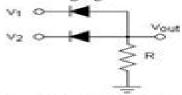
3)



- A) positive logic AND gate
- C) negative logic OR gate

- B) negative logic AND gate
- D) positive logic OR gate
- 4) Name the logic gate that is formed by this circuit.

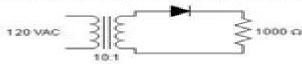
4): _____



- A) positive logic AND gate
- C) positive logic OR gate

- B) negative logic OR gate
- D) negative logic AND gate
- 5) The current flows through the load resistor in this circuit during the

.5) _____



- A) The diode will block all current and there will be no current flowing through the load.
- B) positive half cycle of the input waveform

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