

NOR gate implementation



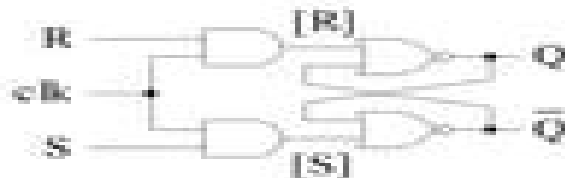
$\overline{S}$	$\overline{R}$	Q	$\overline{Q}$
0	0	Invalid input	
0	1	1	0
1	0	0	1
1	1	Unchanged	

NAND gate implementation

Note that the input is active high for NOR gate implementation, whereas the input is active low for NAND gate implementation.

b. Clocked RS FF

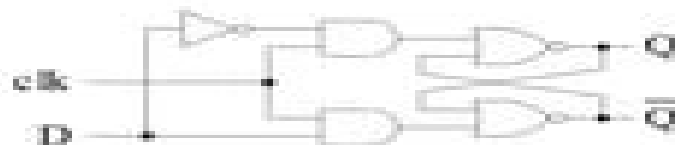
The major problem of RS latch is its susceptibility to voltage noise which could change the output states of the FF. With the clocked RS FF, the problem is remedied. With the clock held low, [S] & [R] held low, the output remains unchanged. With the clock held high, the output follows R & S. Thus the output will be latched its states when the clock goes low.



$\overline{S}$	$\overline{R}$	clk = 1		clk = 0	
		Q_{n+1}	$\overline{Q}_{n+1}$	Q_{n+1}	$\overline{Q}_{n+1}$
0	0	Q_n	$\overline{Q}_n$	Q_n	$\overline{Q}_n$
0	1	0	Q_n	Q_n	$\overline{Q}_n$
1	0	1	$\overline{Q}_n$	Q_n	$\overline{Q}_n$
1	1	Invalid Up		Q_n	$\overline{Q}_n$

c. D-type FF

The D-type FF remedies the indeterminate state problem that exists when both inputs to a clocked RS FF are high. The schematic is identical to a RS FF except that an inverter is used to produce a pair of complementary input.



clk	D	Q_{n+1}
0	x	Q_n
1	0	0
1	1	1

Ecad Lab Manual

Liyong Dong



Ecad Lab Manual:

A Definitive Guide to Logic Circuits and Advanced Circuits Mastering Digital Electronics ,2024-01-18 Introduction The Aims and Objectives of the Book My main aim in writing this book is to introduce you to the exciting and challenging field of digital electronics I want to develop your desire and ability to understand how digital circuits work After reading this book you should be able to do some or all of the following You will understand what TTL and CMOS mean and appreciate their main differences You should know what the five main logic gates are and their respective symbols and Boolean expressions You should know the basics of Boolean algebra and use it to simplify logic expressions and circuits You should know what Karnaugh maps are and how to use them to simplify logic circuits and expressions You should know how to implement the 1st and 2nd canonical formats for Karnaugh maps You will know how the JK flip flop works and how it was born out of the SR latch You should be able to use the JK flip flop and the D type latch to create a series of counters and different shift registers such as SIPO SISO PIPO and PISO You should understand the difference between sequential and combinational logic You should be able to use a range of design techniques that is state diagrams transition tables etc You should be able to create a range of combinational logic circuits such as half and full adders binary subtractors multiplexers etc You should understand how the 555 timer IC works and how to configure it in a range of different applications such as the monostable the astable and PWM You should be able to design a range of logic circuits You should be able to use the ECAD software TINA 12 AF Manual United States. Department of the Air Force,1964 *Subject Guide to Books in Print* ,1971 **IEEE/CPMT International Electronics Manufacturing Technology Symposium** ,2003 **Commerce Business Daily** ,2000-06

Catalog of Copyright Entries. Third Series Library of Congress. Copyright Office,1974 **Sensor Design Using Computer Tools II, April 11-12, 1985, Arlington, Virginia** John A. Jamieson,1985 **Lab Manual for Electronic Devices, Global Edition** THOMAS L. FLOYD,2018-06-19 This laboratory manual is carefully coordinated to the text *Electronic Devices Tenth edition Global edition* by Thomas L Floyd The seventeen experiments correspond to the chapters in the text except the first experiment references Chapters 1 and the first part of Chapter 2 All of the experiments are subdivided into two or three Parts With one exception Experiment 12 B the Parts for the all experiments are completely independent of each other The instructor can assign any or all Parts of these experiments and in any order This format provides flexibility depending on the schedule laboratory time available and course objectives In addition experiments 12 through 16 provide two options for experiments These five experiments are divided into two major sections identified as A or B The A experiments continue with the format of previous experiments they are constructed with discrete components on standard protoboards as used in most electronic teaching laboratories The A experiments can be assigned in programs where traditional devices are emphasized Each B experiment has a similar format to the corresponding A experiment but uses a programmable Analog Signal Processor ASP that is controlled by free Computer Aided Design CAD software from the

Anadigm company www.anadigm.com These experiments support the Programmable Analog Design feature in the textbook The B experiments are also subdivided into independent Parts but Experiment 12 B Part 1 is a software tutorial and should be performed before any other B experiments This is an excellent way to introduce the ASP technology because no other hardware is required other than a computer running the downloaded software In addition to Experiment 12 B the first 13 steps of Experiment 15 B Part 2 are also tutorial in nature for the AnadigmFilter program This is an amazing active filter design tool that is easy to learn and is included with the AnadigmDesigner2 AD2 CAD software The ASP is part of a Programmable Analog Module PAM circuit board from the Servenger company www.servenger.com that interfaces to a personal computer The PAM is controlled by the AD2 CAD software from the Anadigm company website Except for Experiment 12 B Part 1 it is assumed that the PAM is connected to the PC and AnadigmDesigner2 is running Experiment 16 B Part 3 also requires a spreadsheet program such as Microsoft Excel The PAM is described in detail in the Quick Start Guide Appendix B Instructors may choose to mix A and B experiments with no loss in continuity depending on course objectives and time We recommend that Experiment 12 B Part 1 be assigned if you want students to have an introduction to the ASP without requiring a hardware purchase A text feature is the Device Application DA at the end of most chapters All of the DAs have a related laboratory exercise using a similar circuit that is sometimes simplified to make laboratory time as efficient as possible The same text icon identifies the related DA exercise in the lab manual One issue is the trend of industry to smaller surface mount devices which are very difficult to work with and are not practical for most lab work For example almost all varactors are supplied as surface mount devices now In reviewing each experiment we have found components that can illustrate the device function with a traditional one The traditional through hole MV2109 varactor is listed as obsolete but will be available for the foreseeable future from Electronix Express www.elexp.com so it is called out in Experiment 3 All components are available from Electronix Express www.elexp.com as a kit of parts see list in Appendix A The format for each experiment has not changed from the last edition and is as follows Introduction A brief discussion about the experiment and comments about each of the independent Parts that follow Reading Reading assignment in the Floyd text related to the experiment Key Objectives A statement specific to each Part of the experiment of what the student should be able to do Components Needed A list components and small items required for each Part but not including the equipment found at a typical lab station Particular care has been exercised to select materials that are readily available and reusable keeping cost at a minimum Parts There are two or three independent parts to each experiment Needed tables graphs and figures are positioned close to the first referenced location to avoid confusion Step numbering starts fresh with each Part but figures and tables are numbered sequentially for the entire experiment to avoid multiple figures with the same number Conclusion At the end of each Part space is provided for a written conclusion Questions Each Part includes several questions that require the student to draw upon the laboratory work and check his or her understanding of the concepts Troubleshooting questions are

frequently presented Multisim Simulation At the end of each A experiment except 1 one or more circuits are simulated in a Multisim computer simulation New Multisim troubleshooting problems have been added to this edition Multisim troubleshooting files are identified with the suffix f1 f2 etc in the file name standing for fault1 fault2 etc Other files with nf as the suffix include demonstrations or practice using instruments such as the Bode Plotter and the Spectrum Analyzer A special icon is shown with all figures that are related to the Multisim simulation Multisim files are found on the website www.pearsonglobaledition.com Floyd Microsoft PowerPoint slides are available at no cost to instructors for all experiments The slides reinforce the experiments with troubleshooting questions and a related problem and are available on the instructor s resource site Each laboratory station should contain a dual variable regulated power supply a function generator a multimeter and a dual channel oscilloscope A list of all required materials is given in Appendix A along with information on acquiring the PAM As mentioned components are also available as a kit from Electronix Express the kit number is 32DBEDFL10

Government Reports Announcements & Index ,1988 MacUser ,1990-10 *VLSI Design* ,1984
Microtimes ,1996-05 **ECE 311 and ECE 316 Lab Manual** Wayne M. Hope,1999 *Who's who in Engineering* John W. Leonard,Winfield Scott Downs,M. M. Lewis,1948 **Electronic Design Text and Lab Manual** Discovery Press,2005-01-01 **Who's who in America** ,1946 **Electrical Engineering Lab** M. Riaz,1966 *Fundamentals of Electronic Devices and Circuits Lab Manual* David Bell,2009-11-22 The laboratory investigations in this manual are designed to demonstrate the theoretical principles set out in the book *Fundamentals of Electronic Devices and Circuits* 5 e A total of 43 laboratory investigations are offered involving the construction and testing of the circuits discussed in the textbook Each investigation can normally be completed within a two hour period The procedures contain some references to the textbook however all necessary circuit and connection diagrams are provided in the manual so that investigations can also be preformed without the textbook *Electronic Concepts Unit II* ,1974 *Advanced Electronics : Lab Manual, ELE8848* Bourgon, Gilles,Algonquin College,2000

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