



File Utilities Help

File Operation

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Exec



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1 HEADER: 7
2 BGNLIB: 2001 4 19 15 51 38 2005 2 18 15 42 9
3 LIBNAM:PHILIPS.DB
4 UNITS: 0.001 1e-09
5 BGNSTR: 100 5 2 15 40 56 2005 2 18 15 42 9
6 STRNAM:ctrapezoid0
7 BONDRY:
8 LAYER: 36
9 DTATYP: 0
10 XY: 0 0 500 0 200 300 0 300 0 0
11 ENDEL:
12 ENDSTR:
13 BGNSTR: 100 5 2 15 40 56 2005 2 18 15 42 9
14 STRNAM:ctrapezoid1
15 BONDRY:
16 LAYER: 36
17 DTATYP: 0
18 XY: 0 0 200 0 500 300 0 300 0 0
19 ENDEL:
20 ENDSTR:
21 BGNSTR: 100 5 2 15 40 56 2005 2 18 15 42 9
22 STRNAM:ctrapezoid2
23 BONDRY:
24 LAYER: 36
25 DTATYP: 0
26 XY: 0 0 500 0 500 300 300 300 0 0
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Gdsii File Format Manual

Ronnie E. Gillian



Gdsii File Format Manual:

Carafe User's Manual Alvin Jee,1991 Abstract This document describes the user interface for CARAFE the second generation Inductive Fault Analysis IFA program The syntax of all the commands and their parameters are all described in this document along with a description of the formats of the various files used and created by CARAFE Essential Knowledge for Transistor-Level LSI Circuit Design Toru Nakura,2016-05-09 This book is a collection of the miscellaneous knowledge essential for transistor level LSI circuit design summarized as the issues that need to be considered in each design step To design an LSI that actually functions and to be able to properly measure it an extremely large amount of diverse detailed knowledge is necessary Even though one may read a textbook about an op amp for example the op amp circuit design may not actually be possible to complete in one s CAD tools The first half of this text explains important design issues such as the operating principles of CAD tools including schematic entry SPICE simulation layout and verification and RC extraction Then mistake prone topics for many circuit design beginners resulting from their lack of consideration of these subjects are explained including IO buffers noise and problems due to the progress of miniaturization Following these topics basic but very specialized issues for LSI circuit measurement are explained including measurement devices and measurement techniques Readers will have the simulated experience of the whole flow from top to bottom of circuit design and measurement The book will be useful for newcomers to a lab or to new graduates who are assigned to a circuit design group but have little experience in circuit design This published work is also ideal for those who have some experience in circuit design to confirm and complement the knowledge that they already possess **Carafe User's Manual** Alvin Lun-Knep Jee,1994 **Reuse Techniques for VLSI Design** Ralf Seepold,Arno Kunzmann,2012-12-06 Reuse Techniques for VLSI Design is a reflection on the current state of the art in design reuse for microelectronic systems To that end it is the first book to garner the input of leading experts from both research and application areas These experts document herein not only their more mature approaches but also their latest research results Firstly it sets out the background and support from international organisations that enforce System on a Chip SoC design by reuse oriented methodologies This overview is followed by a number of technical presentations covering different requirements of the reuse domain These are presented from different points of view i e IP provider IP user designer isolated reuse intra company or inter company reuse More general systems or case studies e g metrics are followed by comprehensive reuse systems e g reuse management systems partly including business models Since design reuse must not be restricted to digital components mixed signal and analog reuse approaches are also presented In parallel to the digital domain this area covers research in reuse database design Design verification and legal aspects are two important topics that are closely related to the realization of design reuse These hot topics are covered by presentations that finalize the survey of outstanding research development and application of design reuse for SoC design Reuse Techniques for VLSI Design is an invaluable reference for researchers and engineers

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