Information Security and Cryptography

Marc Joye
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Fault Analysis in Cryptography



RJ Alexander

Fault Analysis in Cryptography Marc Joye, Michael Tunstall, 2012-06-21 In the 1970s researchers noticed that radioactive particles produced by elements naturally present in packaging material could cause bits to flip in sensitive areas of electronic chips Research into the effect of cosmic rays on semiconductors an area of particular interest in the aerospace industry led to methods of hardening electronic devices designed for harsh environments Ultimately various mechanisms for fault creation and propagation were discovered and in particular it was noted that many cryptographic algorithms succumb to so called fault attacks Preventing fault attacks without sacrificing performance is nontrivial and this is the subject of this book Part I deals with side channel analysis and its relevance to fault attacks The chapters in Part II cover fault analysis in secret key cryptography with chapters on block ciphers fault analysis of DES and AES countermeasures for symmetric key ciphers and countermeasures against attacks on AES Part III deals with fault analysis in public key cryptography with chapters dedicated to classical RSA and RSA CRT implementations elliptic curve cryptosystems and countermeasures using fault detection devices resilient to fault injection attacks lattice based fault attacks on signatures and fault attacks on pairing based cryptography Part IV examines fault attacks on stream ciphers and how faults interact with countermeasures used to prevent power analysis attacks Finally Part V contains chapters that explain how fault attacks are implemented with chapters on fault injection technologies for microprocessors and fault injection and key retrieval experiments on a widely used evaluation board This is the first book on this topic and will be of interest to researchers and practitioners engaged with cryptographic engineering Fault Diagnosis and Tolerance in Cryptography Luca Breveglieri, 2006-09-28 This book constitutes the refereed proceedings of the Third International Workshop on Fault Diagnosis and Tolerance in Cryptography FDTC 2006 held in Yokohama Japan in October 2006 The 12 revised papers of FDTC 2006 are presented together with nine papers from FDTC 2004 and FDTC 2005 that passed a second round of reviewing They all provide a comprehensive introduction to the issues faced by designers of robust cryptographic devices Progress in Cryptology -- INDOCRYPT 2014 Willi Meier, Debdeep Mukhopadhyay, 2014-10-24 This book constitutes the refereed proceedings of the 15th International Conference on Cryptology in India INDOCRYPT 2014 held in New Delhi India in December 2014 The 25 revised full papers presented together with 4 invited papers were carefully reviewed and selected from 101 submissions. The papers are organized in topical sections on side channel analysis theory block ciphers cryptanalysis efficient hardware design protected hardware design elliptic curves Public-Key Cryptography -- PKC 2013 Kaoru Kurosawa, Goichiro Hanaoka, 2013-02-05 This book constitutes the refereed proceedings of the 16th International Conference on Practice and Theory in Public Key Cryptography PKC 2013 held in Nara Japan in February March 2013 The 28 papers presented together with 2 invited talks were carefully reviewed and selected from numerous submissions The papers are organized in the following topical sections homomorphic encryption primitives functional encryption signatures RSA IBE and IPE key exchange signature schemes

encryption and protocols Automated Methods in Cryptographic Fault Analysis Jakub Breier, Xiaolu Hou, Shivam Bhasin, 2019-03-19 This book presents a collection of automated methods that are useful for different aspects of fault analysis in cryptography The first part focuses on automated analysis of symmetric cipher design specifications software implementations and hardware circuits The second part provides automated deployment of countermeasures The third part provides automated evaluation of countermeasures against fault attacks Finally the fourth part focuses on automating fault attack experiments The presented methods enable software developers circuit designers and cryptographers to test and Fault Tolerant Architectures for Cryptography and Information Security and Privacy, 2005 Hardware Security SIKHAR PATRANABIS, Debdeep Mukhopadhyay, 2018-03-29 This book uses motivating examples and real life attack scenarios to introduce readers to the general concept of fault attacks in cryptography It offers insights into how the fault tolerance theories developed in the book can actually be implemented with a particular focus on a wide spectrum of fault models and practical fault injection techniques ranging from simple low cost techniques to high end equipment based methods It then individually examines fault attack vulnerabilities in symmetric asymmetric and authenticated encryption systems This is followed by extensive coverage of countermeasure techniques and fault tolerant architectures that attempt to thwart such vulnerabilities Lastly it presents a case study of a comprehensive FPGA based fault tolerant architecture for AES 128 which brings together of a number of the fault tolerance techniques presented It concludes with a discussion on how fault tolerance can be combined with side channel security to achieve protection against implementation based attacks The text is supported by illustrative diagrams algorithms tables and diagrams presenting real world experimental results The Crypto Controversy: A Key Conflict in the Information Society Bert-Jaap Koops, 1998-12-16 Cryptography is essential for information security and electronic commerce yet it can also be abused by criminals to thwart police wiretaps and computer searches How should governments address this conflict of interests Will they require people to deposit crypto keys with a trusted agent Will governments outlaw cryptography that does not provide for law enforcement access This is not yet another study of the crypto controversy to conclude that this or that interest is paramount This is not a study commissioned by a government nor is it a report that campaigns on the electronic frontier. The Crypto Controversy is neither a cryptography handbook nor a book drenched in legal jargon The Crypto Controversy pays attention to the reasoning of both privacy activists and law enforcement agencies to the particulars of technology as well as of law to solutions offered both by cryptographers and by governments Koops proposes a method to balance the conflicting interests and applies this to the Dutch situation explaining both technical and legal issues for anyone interested in the subject Classical and Physical Security of Symmetric Key Cryptographic Algorithms Anubhab Baksi, 2022-01-01 This book consolidates several key aspects from the state of the art research in symmetric key cryptography which is among the cornerstones of digital security It presents the content in an informative yet beginner friendly accompanied with toy examples and comprehensible graphics In

particular it highlights the recent developments in tool assisted analysis of ciphers Furthermore promising device dependent attacks such as fault attack and side channel attacks on symmetric key ciphers are discussed in detail One salient feature of this book is to present a detailed analysis of various fault countermeasures. The coverage of our book is guite diverse it ranges from prerequisite information latest research contribution as well as future research directions It caters to students and researchers working in the field of cryptography Financial Cryptography, 2000 *Proceedings of the ... Midwest* Symposium on Circuits and Systems ,2003 Security of Block Ciphers Kazuo Sakiyama, Yu Sasaki, Yang Li, 2016-04-25 A comprehensive evaluation of information security analysis spanning the intersection of cryptanalysis and side channel analysis Written by authors known within the academic cryptography community this book presents the latest developments in current research Unique in its combination of both algorithmic level design and hardware level implementation this all round approach algorithm to implementation covers security from start to completion Deals with AES Advanced Encryption standard one of the most used symmetric key ciphers which helps the reader to learn the fundamental theory of cryptanalysis and practical applications of side channel analysis Public Key Cryptography, 2005 **Classical Cryptography Course** Randall K. Nichols.1996 Cryptography and Coding ,2005 IEICE Transactions on Communications. Electronics. Information, and Systems, 1991-07 Cryptographic Hardware and Embedded Systems ,2003 Proceedings, 2006 Computer Security ,1998 FDTC 2008 Luca Breveglieri, 2008-01-01 Papers from a recent workshop cover all aspects of fault injection based side channel attacks on cryptographic devices and the corresponding countermeasures for both secret and public key cryptosystems Work is arranged in sections on side channel attacks and countermeasures differential fault analysis fault security of hardware and software fault security of elliptic curve cryptography and fault security of public key cryptography Some specific areas examined include silicon level solutions to counteract passive and active attacks comparative analysis of robust fault attack resistant architectures for public and private cryptosystems exploiting hardware performance counters and a generic fault countermeasure providing data and program flow integrity

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